About the Program

The M.S.E.E. program offers students practice-oriented graduate-level education in Electrical and Computer Engineering. Concentrations include Computer Architectures and Microelectronics, Digital Signal Processing and Digital Data Communication, and Intelligent Systems and Control. Current active research projects in the department include embedded systems and system-on-chip design, intelligent interactive tutoring systems, intrusion detection, multisensor fusion, speaker identification, speech processing, and visualization and fault detection in multicasting networks. Other active areas of research include digital signal processing, heat dissipation problems in microchips, human-computer interaction, intelligent multimedia systems, robust and optimal control, and wireless data networks.

Time Limit for Degree Completion: 5 years

Campus Location: Main

Full-Time/Part-Time Status: Students complete the degree program through classes offered after 4:30 p.m. The degree program can be completed on a full- or part-time basis.

Interdisciplinary Study: The program encourages interdisciplinary research with other branches of engineering as well as with various departments in the sciences and applied mathematics. Recent collaborative work with the Department of Computer and Information Sciences includes visualization and fault detection in multicasting networks and image processing.

Areas of Specialization: For each of the three areas of specialization, research includes:

• Computer Architectures and Microelectronics — current practices of computer design and development; hardware realization and integrated circuit layout; MOS-integrated circuit design for high-speed digital computation and data communication; and software-level testing.
• Digital Signal Processing and Digital Data Communication — array signal processing; detection of faults in communication networks; detection of multidimensional signals in the presence of noise; filtering and modulation; intrusion detection, visualization, and security of multicast networks; multisensor data fusion; performance evaluation of local area and wireless networks, broadband networks, and protocols; speaker identification; and voice signal processing.
• Intelligent Systems and Control — intelligent tutoring systems, interactive multimedia, neuro-fuzzy control, and robust and optimal control.

Job Prospects: Graduates with the M.S.E.E. are employed in high-tech industries and government laboratories with responsibilities for design, analysis, and applications of electrical engineering principles. Students who complete an M.S.E.E. with a thesis are prepared to enter a doctoral program.

Non-Matriculated Student Policy: Up to 9 credits of graduate Engineering coursework may be taken at Temple University on a non-matriculated basis and subsequently applied to the M.S.E.E. degree upon admission. If the applicant's undergraduate GPA was less than 3.0, a GPA of 3.25 or better is required on this non-matriculated graduate coursework to receive an admissions exception. Consequently, the Electrical and Computer Engineering (ECE) Graduate Program Director may encourage those with an undergraduate GPA less than 3.0 to take their first three graduate courses prior to making formal application to the M.S.E.E. program. (See the relevant Graduate School policies on special admission procedures for non-matriculated students: 02.23.11.03 (http://www.temple.edu/grad/policies/gradpolicies.htm) and 02.24.19 (http://www.temple.edu/grad/policies/gradpolicies.htm).)

Financing Opportunities: Three forms of financial aid are offered to graduate students:

1. Teaching Assistantship (TA): TA awards are made solely by the Department and require the awardee to work 20 hours per week in support of the Department's undergraduate programs. The TA is compensated with a 9-month stipend, a basic health-insurance plan, and 9 credits per term of tuition remission.
2. Research Assistantship (RA): Individual ECE faculty confer RA awards, using their research funds, upon students who appear well-qualified to carry out the research. Typically, this faculty member becomes the RA's Thesis advisor. The RA normally works up to 20 hours per week and is compensated with a stipend, basic health insurance, and tuition remission.
3. Fellowships: These highly competitive University-wide grants are typically awarded only to Ph.D.-program applicants. See the Engineering, Ph.D. (http://bulletin.temple.edu/archives/2014-2015/graduate/scd/engineering/engineering-phd) program description for details.

Admission Requirements and Deadlines

Application Deadline:

Fall: March 1
Spring: November 1; August 1 international
Applications are processed on a continual basis. Late applications may be considered for admission. Ordinarily, the applicant is informed of an admissions decision within 4 to 6 weeks of receipt of all supporting application documents.

Applicants who plan to matriculate full-time are automatically considered for financial aid awards so no separate application for financial aid is required. To ensure financial aid consideration for the intended term of study, however, applicants should submit a complete application by January 15 (Fall) and August 1 (Spring).

Both admissions and financial aid award decisions originate in the Department of Electrical and Computer Engineering (ECE). Applicants are encouraged to contact the ECE Graduate Program Director for advice and consultation in the application process.

APPLY ONLINE to this graduate program.

Letters of Reference:
Number Required: 3

From Whom: Letters of recommendation should be obtained from college or research faculty who are familiar with the applicant’s competency. If the applicant has an established career in engineering, one of the letters should be provided by the applicant’s immediate supervisor. If the applicant has been out of school long enough that relevant academic reference letters appear impractical, s/he should contact the ECE Graduate Program Director to obtain a waiver of this admission requirement.

Coursework Required for Admission Consideration: Students not adequately prepared for advanced courses may be required to take a number of prerequisites. The ECE Department identifies the needed coursework on a case-by-case basis.

Bachelor’s Degree in Discipline/Related Discipline: A bachelor's degree in Electrical Engineering or Computer Engineering is the preferred prerequisite degree. However, students who have earned a bachelor's degree in a related field are encouraged to apply, with the understanding that remedial preparatory courses may be a pre-condition of admission to the M.S.E.E. program.

University regulations stipulate that the applicant must have earned a 3.0 grade-point average on a 4.0 scale in her/his undergraduate studies, but admission exceptions are made for a variety of circumstances. (See Graduate School Policy 02.23.11.03.) The ECE Graduate Program Director helps the applicant navigate the admission possibilities, including the "Non-Matriculated Student Policy" option.

Statement of Goals: Describe your relevant technical experiences and career goals in one to two pages.

Standardized Test Scores:
GRE scores must be no more than 5 years in advance of the application date. (See Graduate School Policy 02.23.12.) Applicants who require a waiver of the GRE should consult the ECE Graduate Program Director concerning the mechanics and consequences of obtaining an exception.

TOEFL score: 79 iBT or 550 PBT minimum. (See Graduate School Policy 02.23.13.01.)

Resume: Current resume required.

Transfer Credit: Graduate credits taken at an accredited institution prior to matriculation may be transferred into the M.S.E.E. program. In order to transfer, the courses must be equivalent to courses offered at Temple in the student’s area of study and research, and the grades must be “B” or better. The maximum number of credits a student may transfer is 6. (See Graduate School Policy 02.24.21.)

Test Waivers: Applicants with two or more years of employment in an engineering profession performing engineering design and analysis may request a waiver of the GRE. Consult with the ECE Graduate Program Director concerning the mechanics and consequences of obtaining an exception.

Program Requirements

General Program Requirements:
Number of Credits Required Beyond the Baccalaureate: 30

Students choose between three tracks:

1. Thesis Track, which is intended for full-time students who have a financial aid award and includes 24 s.h. of didactic coursework and 6 s.h. of thesis (ECE 9996).
2. Project Track, which is intended for full-time students who are self-supporting and includes 27 s.h. of didactic coursework and 3 s.h. of project (ECE 9995).
3. Coursework Track, which is intended for self-supporting part-time students and entails 30 s.h. of didactic coursework.

In the first term, the student and ECE Graduate Program Director jointly establish which track the student will follow; in doing this, they initiate the "M.S.E.E. Plan of Study." The Plan of Study form lists all required courses and suggests an M.S.E.E. program requirement execution sequence for the
student to follow. This form is used to track the student's progress, and is updated and annotated at least once a year as the student completes the various benchmarks in the M.S.E.E. program.

If a student's circumstances change, s/he can change tracks by revising the Plan of Study form and obtaining the requisite approval signatures. However, when considering whether to change one's track, the student should note that:

- "Thesis" credits (ECE 9996) can only be applied toward the Thesis M.S.E.E. degree program and cannot be applied to either the Project or Coursework Tracks.
- "Project" credits (ECE 9995) can only be applied toward the Project M.S.E.E. degree program and cannot be used for either the Coursework or Thesis Tracks.

In all three options, the didactic coursework may include up to, but no more than, 3 s.h. of ENGR 9182 Independent Study I or 3 s.h. of ECE 9991 Directed Research. Furthermore, students who wish to take graduate coursework in Temple University schools/colleges other than the College of Engineering will need to obtain the appropriate written approvals on their Plan of Study form.

Culminating Events: Depends on the student's choice of track: Thesis, Project, or Coursework.

Thesis Option:
The master's thesis is the culminating event in the Thesis Track and is typically undertaken during the last two successive terms of study. Successful completion requires the following:

1. Thesis Proposal — ECE 9996 Thesis I (3 s.h.)
   The student assembles a committee of three or more faculty members, including her/his advisor, who is typically a full-time ECE faculty member. The student's Plan of Study should be updated, if necessary, to indicate the advisor's name.
   Under the guidance of the advisor and committee, the student prepares a research proposal and presents her/his proposal in an open College-wide seminar. The student is responsible for scheduling the proposal and posting an announcement at least 10 business days in advance of this seminar.
   Ordinarily, the proposal seminar is immediately followed by a meeting of the student's advisory committee in which the student is closely questioned about the details and strategy of the proposed research. The proposal is then accepted by the committee, accepted by the committee with revisions, or rejected by the committee.
   The student's advisory committee also jointly determines the letter grade (A-F) for Thesis I at the end of the term. The student must pass Thesis I before registering for Thesis II. If the student fails Thesis I, s/he may either re-register for Thesis I in the next regular term and repeat the entire proposal process (noting that a second failure will result in automatic dismissal from the University) or consider switching to the Project or Coursework Track, with the relevant updating of the Plan of Study form.

2. Thesis Defense — ECE 9996 Thesis II (3 s.h.)
   The student should register for Thesis II in the term that s/he is prepared to defend the thesis. The thesis document should be prepared in a format compliant with University standards. (See Graduate School Policy 02.26.12.02 (http://www.temple.edu/grad/policies/gradpolicies.htm).) The student should provide her/his committee with a copy of the completed thesis at least two weeks before the date of the thesis defense.
   The thesis is scheduled during a regular academic term, including summer terms. It should not be scheduled during study days, final exams, or the breaks between terms. The student should arrange for, and post an announcement of, the thesis defense at least 10 business days in advance of the defense. Furthermore, if the student is to graduate in the same term that s/he defends the thesis, the defense should be scheduled no later than 30 days prior to the end of the term to allow for document revisions in keeping with Graduate School deadlines, as specified at www.temple.edu/dissertationhandbook/deadlines.html.
   The thesis defense is an open College seminar in which the student presents the concepts and results of her/his research. Normally, this presentation is immediately followed by a meeting of the thesis committee, which closely examines the student's research. The committee can accept the thesis as provided, accept the thesis with revisions, or not accept the thesis. If the thesis is accepted, the committee jointly decides on a letter grade for Thesis II. If the thesis is not accepted, but the committee decides not to fail the student:
   a. an "R" grade is assigned to Thesis II;
   b. the student registers in each subsequent term for one credit of ENGR 9991 Directed Research until s/he is again prepared to attempt the defense; and
   c. the entire open-seminar defense procedure described above is carried out in the term that the student is prepared to defend the thesis.

Project Option:
The project is the culminating event in the Project Track. It is normally carried out in the student's last term of study. The student selects an advisor (usually a full-time faculty member in the ECE Department), registers for ECE 9995 Project, and conducts a one-term research activity under the supervision of the advisor. Near the end of the term, the student prepares a report of her/his findings and presents the study in an open departmental seminar. Both the seminar and the written report are used to determine the student's grade for ECE 9995. The grade is jointly determined by the advisor and a second grader selected by the ECE Graduate Program Director, as recorded in the Plan of Study.

Coursework Option:
No culminating event is warranted for the Coursework Track.
Contacts

Program Web Address:
http://engineering.temple.edu/electrical-computer-engineering/graduate-programs

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Courses

ECE 5314. Microelectronics. 3 Credit Hours.
Advanced study of electronic devices and their applications to linear, non-linear, and digital circuits; transistors, FET's, amplifiers, digital integrated circuits, and VLSI's; Software design emphasized. A term project will be assigned.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits.

ECE 5324. VLSI System Design and Testing. 3 Credit Hours.
An introduction to a hierarchical design methodology of VLSI; study of basic logic elements and design methods in nMOS and CMOS; development of testable designs; the physics of MOS devices and fabrications processes; design rules and computation of circuit parameters from layout; system level design techniques; circuit structures with built-in self-test, design-for-test and self-checking features.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits
Pre-requisites:
ECE 5314|Minimum Grade of C|May not be taken concurrently.

ECE 5412. Control System Analysis. 3 Credit Hours.
Review of control concepts and application; state space representation of dynamical systems; controllability, observability; time invariant and time varying systems, design of full state feedback and output feedback systems; eigenstructure assignment; the linear quadratic regulator; Kalman filter; estimation and filtering; robust control via eigenstructure design, Kharitonov theorem, application examples.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits.
ECE 5512. Intro Digital Comm. 3 Credit Hours.
Baseband pulse, digital, and passband communications systems; properties and bandwidth of signals and noise; detection of signals in noise; signal-to-noise ratio (SNR); distortionless transmission and intersymbol interference; pulse code modulation; amplitude, phase and frequency modulation and demodulation; simulation of communication systems.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits.

ECE 5514. Digital Signal Processing Analysis. 3 Credit Hours.
Topics covered are: various types of digital signal processing (DSP) techniques such as convolution, correlation, and filtering, as well as Finite Impulse Response (FIR) and Infinite Impulse Response (IIR) all pass and comb digital filters, the Discrete Fourier Transform, and the use of MATLAB as a tool for DSP software tasks. A term project will be assigned.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits.

ECE 5516. Introduction to Communication Networks. 3 Credit Hours.
Introduction to Internet and TCP and IP protocols, telephone networks, Local Area Networks, packet switching, ATM, and other related topics.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits.

ECE 5522. Engineering Principles of Computer Intrusion and Detection. 3 Credit Hours.
This course provides an introduction of computer intrusion and detection techniques. It gives theoretical and practical foundations necessary to continue further learning of computer security. We will study and analyze critical security vulnerabilities of software design and network and information systems. The learned skills are widely used by IT security analysts in industries. At the end of the class the students will be able to understand basic concepts of intrusion detection and traffic analysis from a practical point of view. This course will provide the tools and knowledge necessary to continue further learning in computer security and advance further in the profession.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits

Pre-requisites:
ECE 5516 (Minimum Grade of B-) May not be taken concurrently.

ECE 5528. Introduction to Cryptography and Information Security. 3 Credit Hours.
This course covers the theory and practice of computer communications security. Topics include symmetric encryption, public and private key cryptography, message digests, digital signatures, secure email, and various types of authentication methods. We will review various cryptographic primitives, algorithms, intrusion attacks, and security protocols.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits.

ECE 5512. Advanced Processor Systems. 3 Credit Hours.
Hardware description language (Verilog) design of processor systems for digital signal processing and data communication. Projects will be assigned in simulation and synthesis of dataflow and processor architectures targeting field programmable gate arrays (FPGA).
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits.

ECE 5622. Introduction to Computer Architecture. 3 Credit Hours.
Instruction set architectures, Register Transfer Level hardware description. Data-path design. Controller design. Caches and memory systems. Addressing. Microprogramming. Computer arithmetic. Survey of current computers and microprocessors. Projects will include Verilog/VHDL implementation of data-path components and testing them on FPGAs.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits.

ECE 5712. Power Systems Engineering. 3 Credit Hours.
This course introduces the modern power systems and its changing landscape. The course covers the basics of power generation and transformers, and an introduction to power electronic devices, AC transmission and distribution, power flow, economic dispatch, transient and stability analysis, short circuit analysis, and HVDC systems, power system protection, power market deregulation.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits.

ECE 5714. Introduction to Intelligent Systems Engineering. 3 Credit Hours.
Introduction of the use of artificial intelligence techniques to develop intelligent systems. The course gives the student 1) an overview of what artificial intelligence is and its current state; 2) an overview of intelligent systems --what they are and their possible future role in society; 3) a practical and theoretical knowledge of expert systems, their development, implementation and maintenance and 4) an introduction to intelligent tutoring systems and to provide a perspective about the potential impact of these systems.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits.
ECE 5732. Electric Machines and Drives. 3 Credit Hours.
Fundamentals of electromechanical energy conversion, electric drives and systems. Transformers, DC machines, synchronous machines, induction motors, dq-transformation, vector control of induction motors, reluctance motors, single phase motors, brushless dc motor. Introduction to power electronics and their applications in power drives.

Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits.

ECE 8110. Special Topics in Electrical and Computer Engineering. 3 Credit Hours.
Selected advanced topics in various major research areas under electrical and computer engineering.

Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may be repeated for additional credit.

ECE 8324. Mixed Signal VLSI Design. 3 Credit Hours.
Basic MOS device physics, single state amplifiers, frequency response, op amps, switched capacitor circuits, short-channel effects, amplifier design for wireless communication, low power static RAM architectures, layout and packaging.

Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits

Pre-requisites:
ECE 5324|Minimum Grade of C|May not be taken concurrently.

ECE 8334. Nano Applications, MEMS & NEMS. 3 Credit Hours.

Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits

Pre-requisites:
ECE 5324|Minimum Grade of C|May not be taken concurrently.

ECE 8412. Optimal and Robust Control. 3 Credit Hours.
Concept of optimality, calculus of variations, Euler-Lagrange equation, Pontryagin's minimum principle, Bellman's equation, Kalman filter, uncertainties in physical systems; structured and unstructured uncertainties; application of the Lyapunov method to robust control problems; robust optimal control; state space design for finite and infinite horizon problems; H-infinity design.

Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits

Pre-requisites:
ECE 5412|Minimum Grade of C|May not be taken concurrently.

ECE 8414. Adaptive Control. 3 Credit Hours.

Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits.

ECE 8512. Signal Processing and Communication Theory. 3 Credit Hours.
Coherent and non-coherent detection of binary and M-ary signals in noise; waveform coding, linear block coding; convolutional, cyclic and turbo codes; error probability and bandwidth-efficiency plane in the design of digital communications systems; multipath and fading channels; simulation of communication systems.

Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits

Pre-requisites:
(ECTE 5512|Minimum Grade of C|May not be taken concurrently
AND ENGR 5033|Minimum Grade of C|May not be taken concurrently).

ECE 8514. Applications in Digital Signal Processing. 3 Credit Hours.
FIR and IIR digital filter design, finite word length effects, filter banks, multirate signal processing, spectral analysis (classical, modern, parametric and nonparametric techniques), adaptive filtering (Wiener filter theory) and speech production, analysis, and processing tools and speech coding. Computer experiments using MATLAB will be an integral part of the course.

Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits

Pre-requisites:
ECE 5033|Minimum Grade of C|May not be taken concurrently.

ECE 8516. Design and Performance of Communication Networks. 3 Credit Hours.
An overview of the technologies, architectures and protocols used to build high-speed communication networks. Design and performance analysis techniques for computer communication networks. Topics will include: design and performance analysis of wired and wireless local networks, sensor networks, and Internet. Projects will include developing stochastic models, queueing analysis, and simulations.

Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits.
ECE 8524. Speech Signal Processing. 3 Credit Hours.
Spectral analysis of non-stationary signals, short-time Fourier transform, homomorphic filtering and filter bank, Speech compression, and synthesis techniques. Weiner filtering for speech enhancement.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits
Pre-requisites:
ECE 8514|Minimum Grade of C|May not be taken concurrently.

ECE 8525. Fundamentals of Speech Recognition. 3 Credit Hours.
This course introduces students to the theory and implementation of modern day speech recognition systems. We begin with a review of pattern recognition and machine learning, including topics such as Gaussian mixture models and Bayesian models. We then discuss the three main components of a speech recognition system: feature extraction, acoustic modeling and language modeling. We conclude the course with an overview of state of the art systems. Students will learn how to simulate and evaluate complex machine learning algorithms such as hidden Markov models and neural networks. Data-driven methodologies will be emphasized.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits
Pre-requisites:
(ENGR 5022|Minimum Grade of B-|May not be taken concurrently)
AND (ENGR 5033|Minimum Grade of B-|May not be taken concurrently).

ECE 8526. Information Theory. 3 Credit Hours.
Information Theory is a field that has been central to the development of modern communications and computing technologies. The goal of this course is to provide the student with a thorough understanding of the concepts of entropy and information, and how to apply these to real world problems such as speech recognition, language engineering, signal compression, and financial modeling. A secondary goal is to develop a mathematically rigorous understanding of methods for measuring and manipulating various measures of information in signals and systems.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits
Pre-requisites:
(ENGR 5022|Minimum Grade of B-|May not be taken concurrently)
AND (ENGR 5033|Minimum Grade of B-|May not be taken concurrently).

ECE 8527. Introduction to Machine Learning and Pattern Recognition. 3 Credit Hours.
Pattern recognition theory and practice is concerned with the design, analysis, and development of methods for the classification or description of patterns, objects, signals, and processes. At the heart of this discipline is our ability to infer the statistical behavior of data from limited data sets, and to assign data to classes based on generalized notions of distances in a probabilistic space. Many commercial applications of pattern recognition exist today, including voice recognition, fingerprint classification, and retinal scanners. Recent developments in statistical modeling using Bayesian techniques, neural networks, decision trees, fuzzy logic, and syntactic structures have accelerated the growth of pattern recognition applications. The objective of this course is to introduce fundamental methods of pattern recognition, both statistical and neural, with examples from several application areas.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits
Pre-requisites:
(ENGR 5022|Minimum Grade of B-|May not be taken concurrently)
AND (ENGR 5033|Minimum Grade of B-|May not be taken concurrently).

ECE 8528. Advanced Topics in Statistical Modeling for Engineering Applications. 3 Credit Hours.
This course builds on a basic knowledge of machine learning and reviews recent advances in the field. It is a research-oriented course intended to complement a student's thesis or dissertation research. The course will focus on a selection of emerging machine learning algorithms and analyze contemporary publications on these techniques. The emphasis will be on algorithms suited to large, complex data sets. Both supervised and unsupervised learning methodologies will be discussed. Applications will be drawn from several signal processing disciplines including speech, image and bioengineering applications.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits
Pre-requisites:
(ENGR 5022|Minimum Grade of B-|May not be taken concurrently)
AND (ENGR 5033|Minimum Grade of B-|May not be taken concurrently)
AND (ECE 8527|Minimum Grade of B-|May not be taken concurrently).
ECE 8529. Fundamentals of EEG Processing. 3 Credit Hours.
Electroencephalography (EEG) records electrical activity along the scalp, measuring spontaneous electrical activity of the brain. The signals measured along the scalp can be correlated with brain activity, which makes it a primary tool for diagnosis of brain-related illnesses. EEG specialists review these waveforms and develop a diagnosis. EEGs traditionally have been used to diagnose epilepsy and strokes. Other medical applications of brain waves include the diagnosis of coma, encephalopathies, brain death and sleep disorders. EEGs are increasingly being used to diagnose head-related trauma injuries and Alzheimer's disease. Hence, there is a growing need for expertise to interpret EEGs and, equally important, to understand how these conditions manifest themselves in the EEG signal. In this course we will discuss the techniques neurologists use to manually interpret EEGs. A vast archive of clinical EEG recordings will be studied. Since EEG signals are very low-level electrical signals, we will then discuss digital signal processing that is used to convert the raw electrical signals into visualizations that can be readily interpreted. We will also introduce machine learning techniques that are used to automatically interpret and transcribe these signals.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits.

ECE 8622. Advanced Computer Architecture. 3 Credit Hours.
Advanced course in the design and analysis of computer architecture. Topics will include instruction level parallelism, digital signal processors, network processors and multi-microprocessors. Projects will focus on the design, design analysis and FPGA implementations of computing systems.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits
Pre-requisites:
ECE 5322 | Minimum Grade of C | May not be taken concurrently.

ECE 9324. VLSI Physical Design. 3 Credit Hours.
This course provides a comprehensive background in the principles and algorithms of VLSI physical design. The algorithms are presented in an intuitive manner so that the student can concentrate on the basic idea of the algorithms. The students are provided enough details to implement the algorithms.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits
Pre-requisites:
(ECE 8324 | Minimum Grade of C | May not be taken concurrently)
AND (ECE 5324 | Minimum Grade of C | May not be taken concurrently).

ECE 9412. Nonlinear Control System. 3 Credit Hours.
Modeling of nonlinear systems, types of nonlinearity; Phase Plane Analysis, construction of phase portrait, limit cycle, saddle point; Existence and uniqueness of solutions, sensitivity; Lyapunov Stability, region of attraction, construction of Lyapunov functions; Perturbation Analysis variation of parameters, Method of averaging, Describing Functions, frequency domain analysis; Sliding Mode Control, sliding surface; Feedback Linearization, Lie algebra, state and output linearization, applications.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits
Pre-requisites:
ECE 8412 | Minimum Grade of C | May not be taken concurrently.

ECE 9512. Detection, Estimation, and Modulation Theory. 3 Credit Hours.
Signal detection and estimation in white and non-white noise, MAP estimation, applications in data and telecommunications. Wiener and Kalman-Bucy filters, linear and non-linear modulation.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits
Pre-requisites:
ENGR 5033 | Minimum Grade of C | May not be taken concurrently.

ECE 9514. Adaptive Signal Processing. 3 Credit Hours.
Adaptive filter techniques such as Weiner filter, Linear Prediction, Least-Mean-Square, Recursive Least-Squares, Kalman Filtering algorithms. Introduction to the application of adaptive filters to communications, control, and speech processing.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits
Pre-requisites:
(ECE 5514 | Minimum Grade of C | May not be taken concurrently)
AND ECE 8514 | Minimum Grade of C | May not be taken concurrently.

ECE 9524. Digital Image Processing. 3 Credit Hours.
P2D digital filters, digital image edge detection and segmentation, feature extraction, deblurring, wavelet transforms, JPEG image compression, Fourier optics.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits
Pre-requisites:
ECE 8514 | Minimum Grade of C | May not be taken concurrently.
ECE 9622. Parallel Processing Architectures. 3 Credit Hours.
This course provides an in-depth study of the design, engineering, and evaluation of modern parallel computers. Design issues covered include: naming, replication, synchronization, latency, overhead, and bandwidth. Other topics include scalable multiprocessors and interconnection network design.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may not be repeated for additional credits
Pre-requisites:
ECE 8612|Minimum Grade of C|May not be taken concurrently.

ECE 9991. Directed Research. 1 to 6 Credit Hour.
Under the guidance of a faculty member, students will select a topic in electro-technology to be researched using at least five references. An extensive research paper must be submitted which will be reviewed by two faculty members. The project report must also be presented at an open seminar. Projects related to industrial applications are encouraged. For non-thesis students only.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may be repeated for additional credit.

ECE 9995. Project. 1 to 3 Credit Hour.
Under the guidance of a faculty member, students will select a topic in electro-technology to be researched using at least five references. Student present the research at an open seminar, and submits an extensive research paper, which will be reviewed by two faculty members. Projects related to industrial applications are encouraged. For non-thesis students only.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may be repeated for additional credit.

ECE 9996. Thesis. 1 to 3 Credit Hour.
Under the guidance of a faculty member, students will select a topic in electro-technology, and conduct research leading to submission and oral presentation of a thesis proposal and the final defense of the thesis. For thesis students only.
Level Registration Restrictions: Must be enrolled in one of the following Levels: Graduate
Repeatability: This course may be repeated for additional credit.